

FIG. 1 (PRIOR ART)

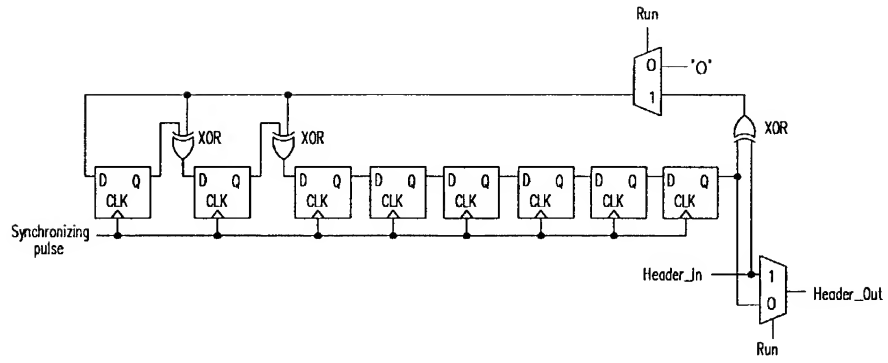


FIG. 2A(PRIOR ART)

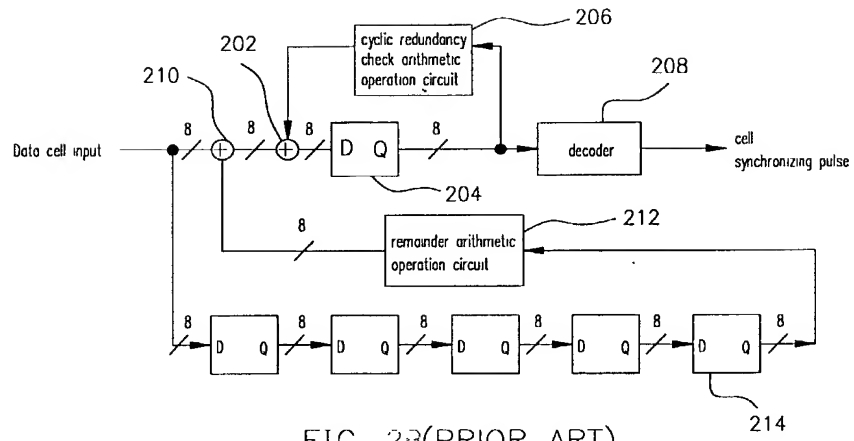


FIG. 2B(PRIOR ART)

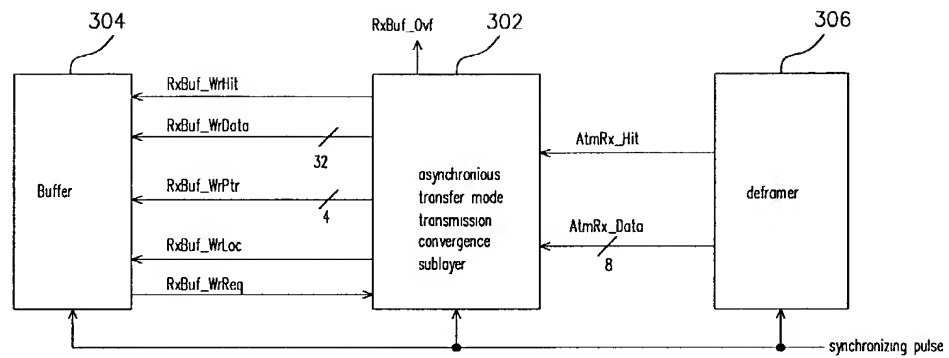


FIG. 3

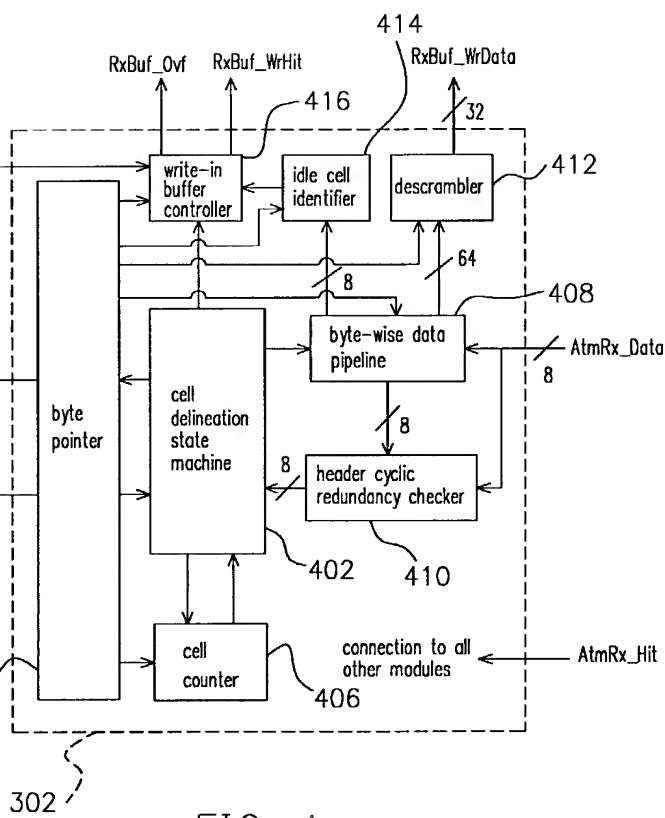


FIG. 4

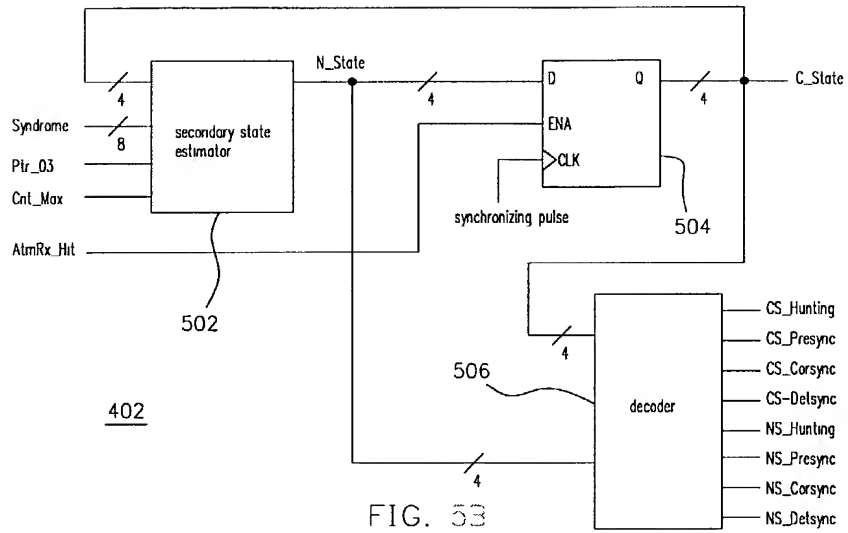


FIG. 53

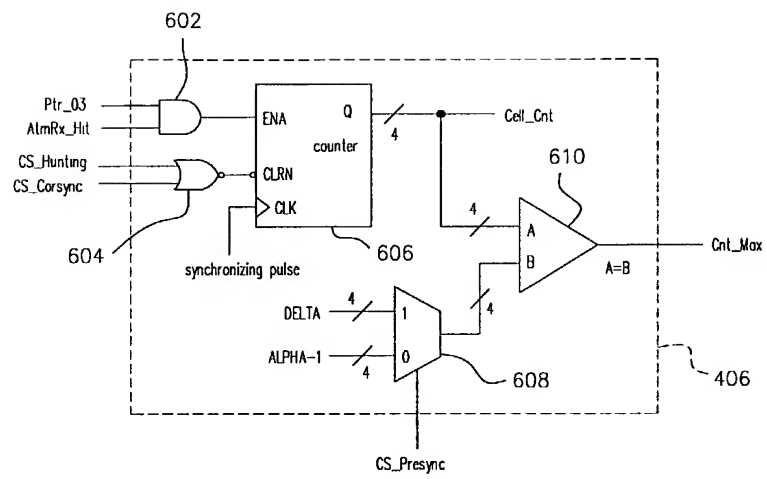
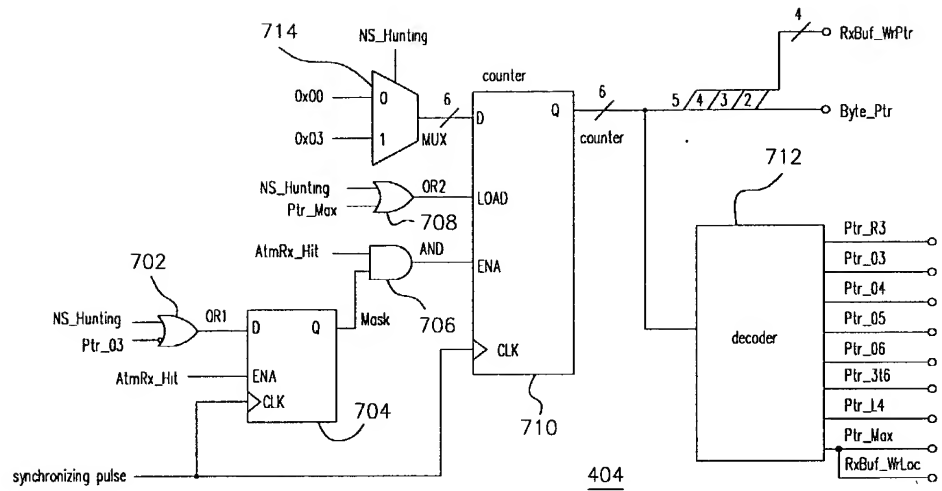


FIG. 6



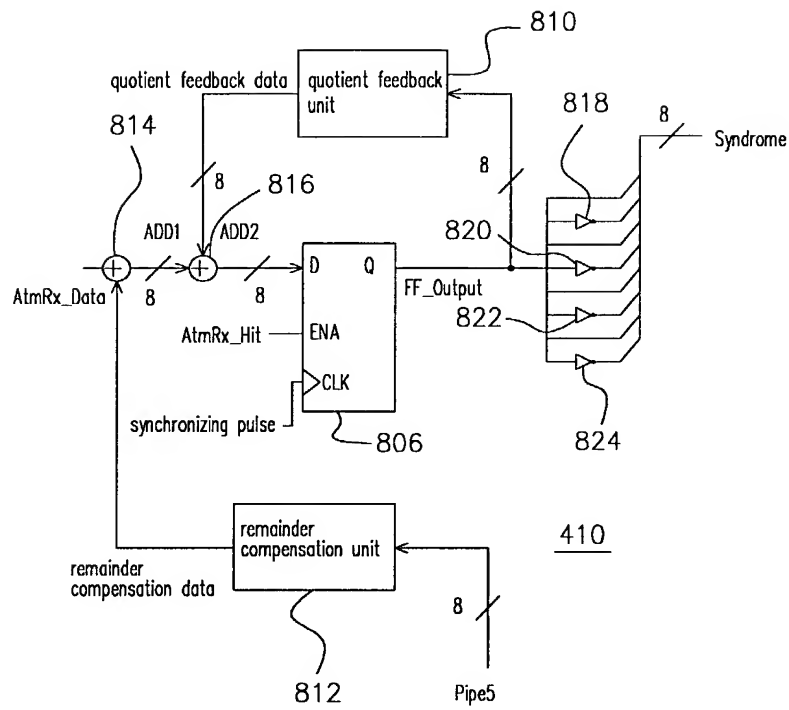


FIG. 8A

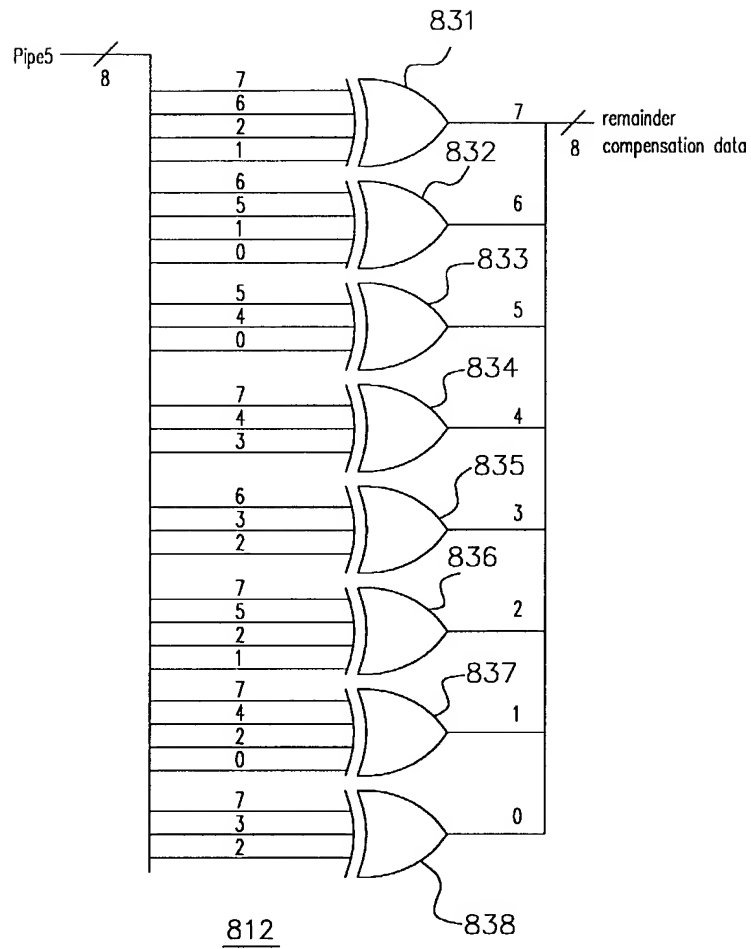


FIG. 8B

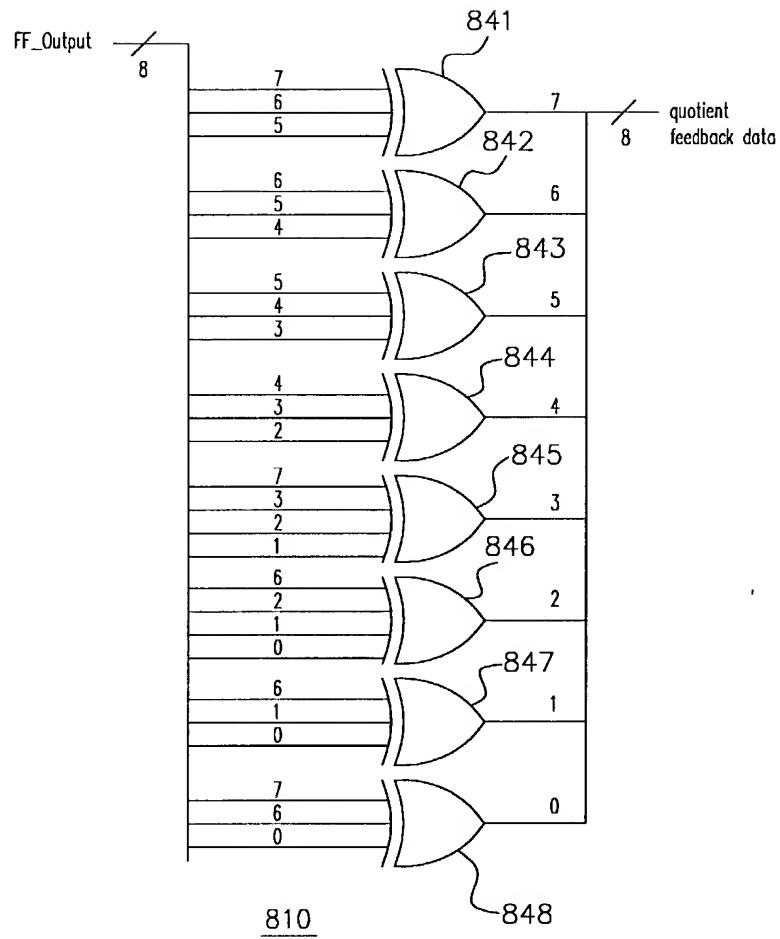
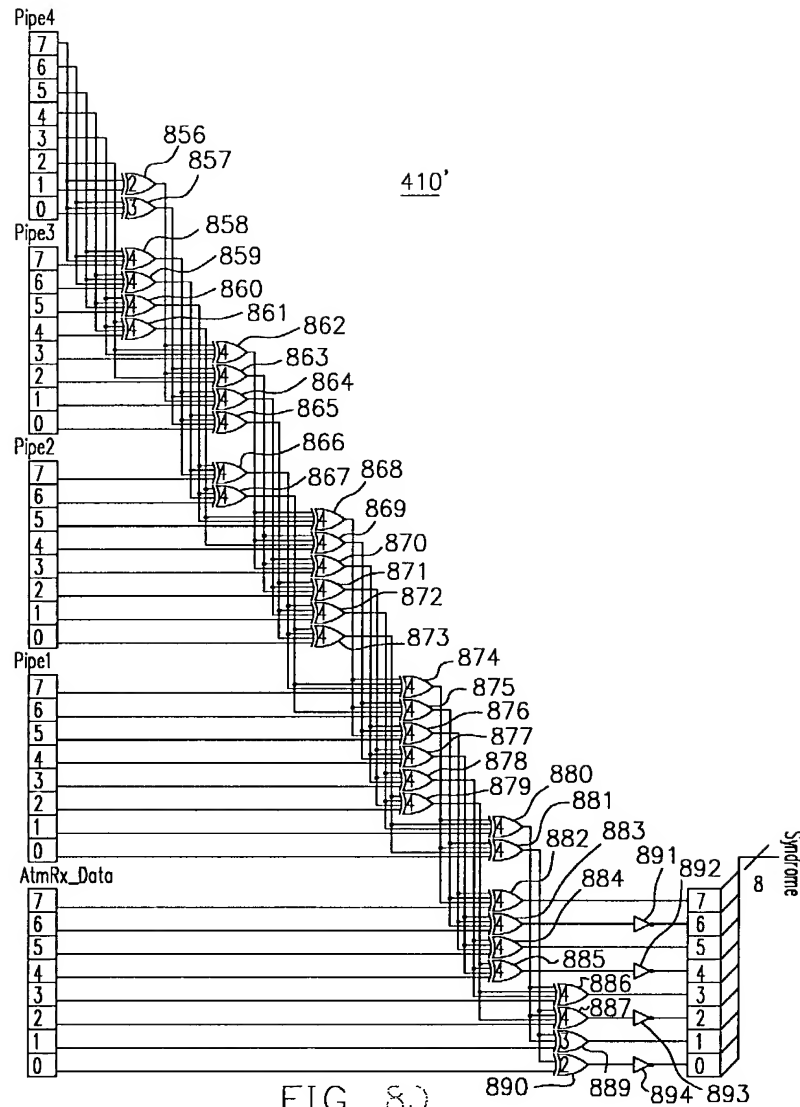


FIG. 80



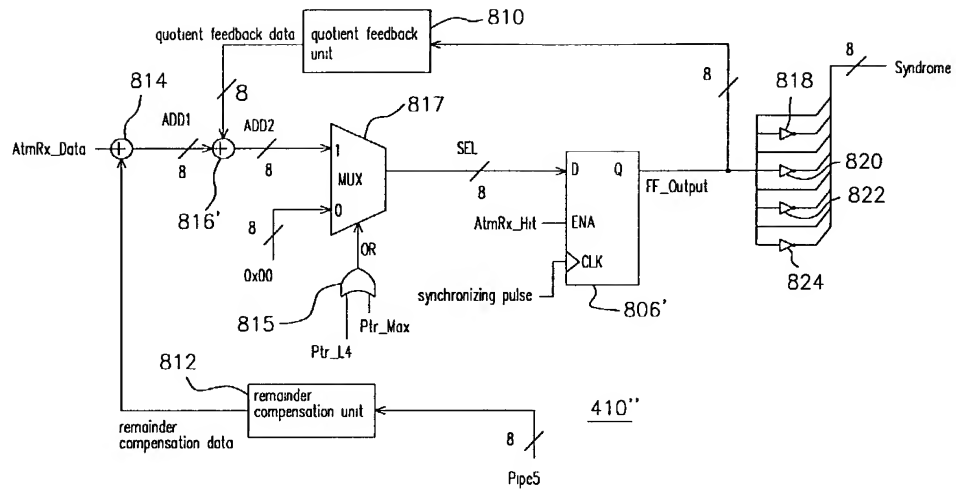


FIG. 8

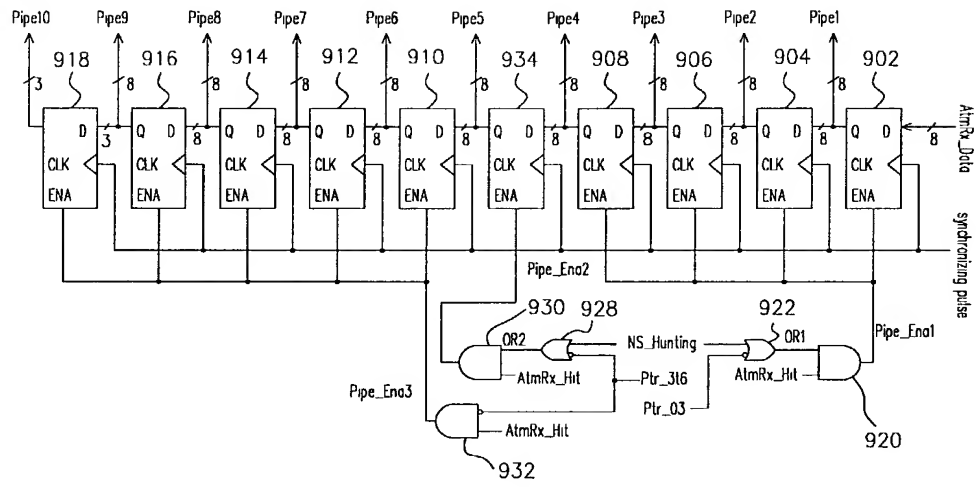
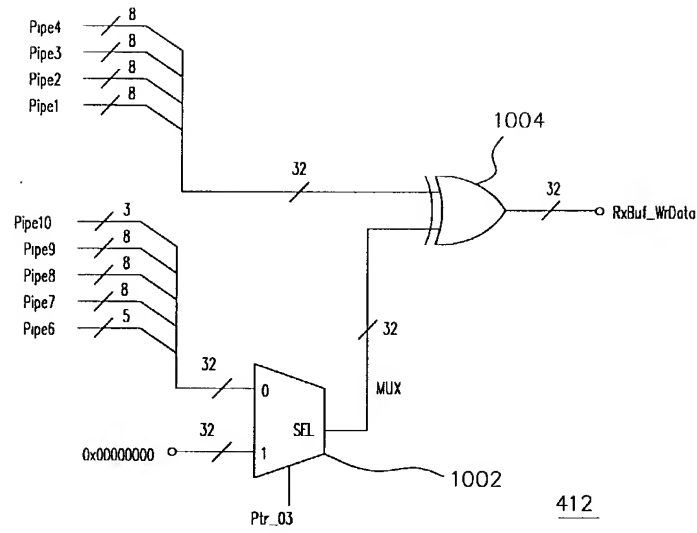


FIG. 9

408



H0	H1	H2	H3	HEC			
0x00	0x00	0x00	0x01	0x52	0x6a	0x6a	0x6a
5-byte header cell					48-byte payload cell		

FIG. 1A

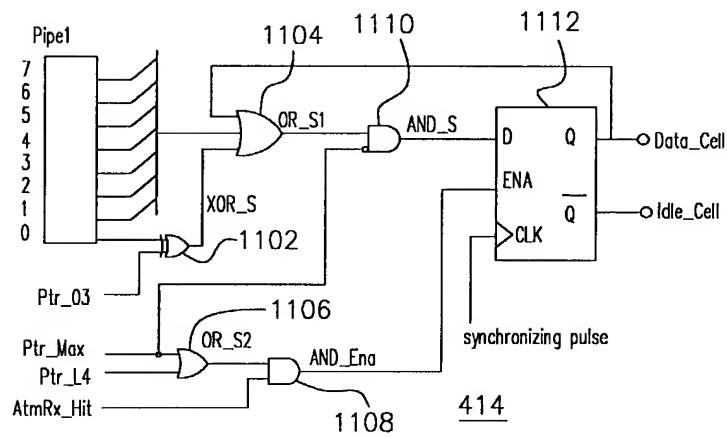


FIG. 1B

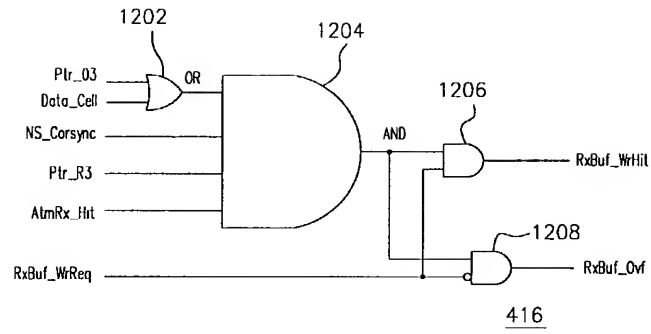


FIG. 12

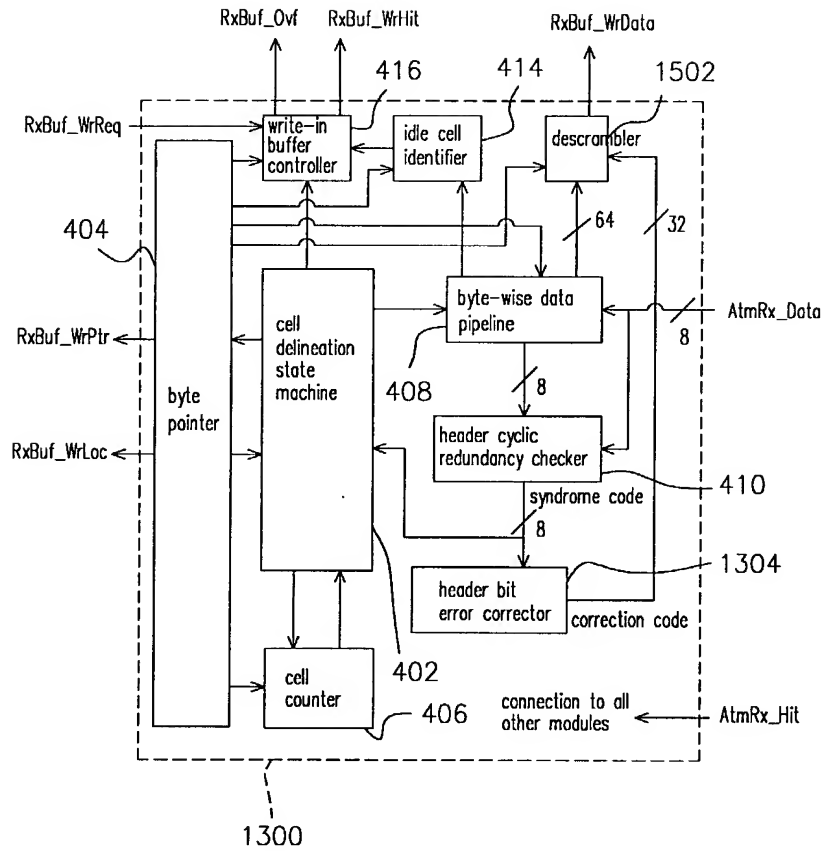


FIG. 13

8-bit syndrome code	32-bit correction code
0x07 0x0e 0x1c 0x38	0x00000001 0x00000002 0x00000004 0x00000008
0x70 0xe0 0xc7 0x89	0x00000010 0x00000020 0x00000040 0x00000080
0x15 0x2a 0x54 0xa8	0x00000100 0x00000200 0x00000400 0x00000800
0x57 0xae 0x5b 0xb6	0x00001000 0x00002000 0x00004000 0x00008000
0x6b 0xd6 0xab 0x51	0x00010000 0x00020000 0x00040000 0x00080000
0xa2 0x43 0x86 0x0b	0x00100000 0x00200000 0x00400000 0x00800000
0x16 0x2c 0x58 0xb0	0x01000000 0x02000000 0x04000000 0x08000000
0x67 0xce 0x9b 0x31	0x10000000 0x20000000 0x40000000 0x80000000
default	0x00000000

FIG. 14

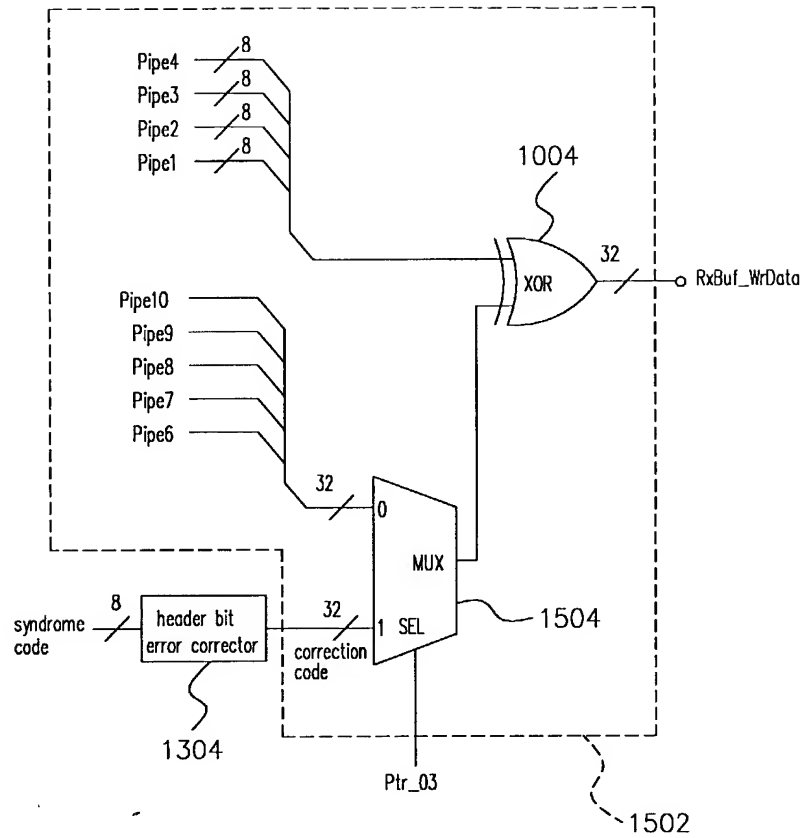


FIG. 15